

1. Scope :

This specification applies to P/N silicon TVS diode chips,
Device NO. SD-14108

2. Structure :

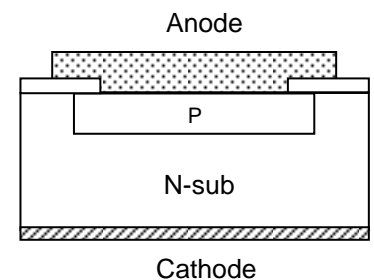
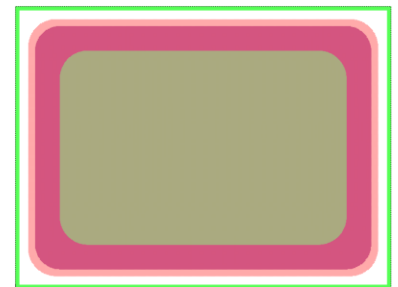
- 2-1. Planar type : P/N Diode.
- 2-2. Electrodes :
Top side : Aluminum alloy.
Back side : Gold layer.

3. Size :

- 3-1. Chip size : 47.2 mils x 35.4 mils (1.200 mm x 0.900 mm).
- 3-2. Chip thickness : 5.9 ± 1.0 mils (0.150 ± 0.0254 mm)
- 3-3. Bonding pad : 42.5 mils x 30.7 mils (1.080 mm x 0.780 mm).
- 3-4. Pattern drawing : Refer to the attached drawing.

4. Electrical characteristics (Ta = 25 °C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Reverse Working Voltage	V _{RWM}			5	V	-
Reverse Current	I _R			2000	nA	V _R =5V Ee=0mW/cm ²
Reverse Breakdown Voltage	V _(BR)	7		9	V	I _R =5mA Ee=0mW/cm ²
Junction capacitance	C		1800		pF	F=1MHZ, V _R =0V
Reverse Clamping Voltage <i>*IEC6100-4-5 Standard.</i>	V _C		7.43		V	I _{PP} =1A T _p =8/20us
			7.80		V	I _{PP} =4A T _p =8/20us
Forward Voltage	V _f			1.2	V	I _F =10mA Ee=0mW/cm ²



Parameter	Symbol	Rating	Unit
ESD Voltage Air <i>*IEC6100-4-2 Standard.</i>	V _{ESD}	30	KV
ESD Voltage Contact <i>*IEC6100-4-2 Standard.</i>		30	

*AEC-Q101 qualified.